

Notice of References Cited	Application/Control No. 10/772,555		Applicant(s)/Patent Under Reexamination SMITH ET AL.	
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U.S. PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Quinn Able Jacobson, "High-Performance Frontends For Trace Processors", 1999, Univ. of Wisconsin - Madison, pp. 1-225			
	V	Ashutosh Sham Dhodapkar, "Autonomic Management of Adaptive Microarchitectures", 2004, Univ. of Wisconsin - Madison, pp. 1-154			
	W	Smith et al., "Dynamic Microarchitecture Adaptation via Co-Designed Virtual Machines", ISSCC 2002, 2002 IEEE, pp. 1-2			
	X	Dhodapkar et al., "Comparing Program Phase Detection Techniques", Dec. 2003, IEEE, pp. 1-11			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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	R					
	S					
	T					

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	U	Dhodapkar et al., "Tuning Reconfigurable Microarchitectures for Power Efficiency", 2004 IEEE, pp. 1-7			
	V				
	W				
	X				

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